

CameraLink



CameraLink is an industrial image processing interface for fast image transmissions which was introduced in the year 2000. The standard is managed by the AIA (Automated Imaging Association) in the USA.

Power over CameraLink (to power the camera via the cable) was released with the version 1.2 of the standard.

CameraLink provides different configurations which supports different clock frequencies:

- BASE: max. 24 bit per clock
- MEDIUM: max. 48 bit per clock
- FULL: max. 64 bit per clock

The mvHYPERION family offers suitable frame grabbers from BASE to FULL. Equipped with a FPGA and internal image memory, our frame grabbers relieve the host system and ensure a smooth operation.